

FIG. 1

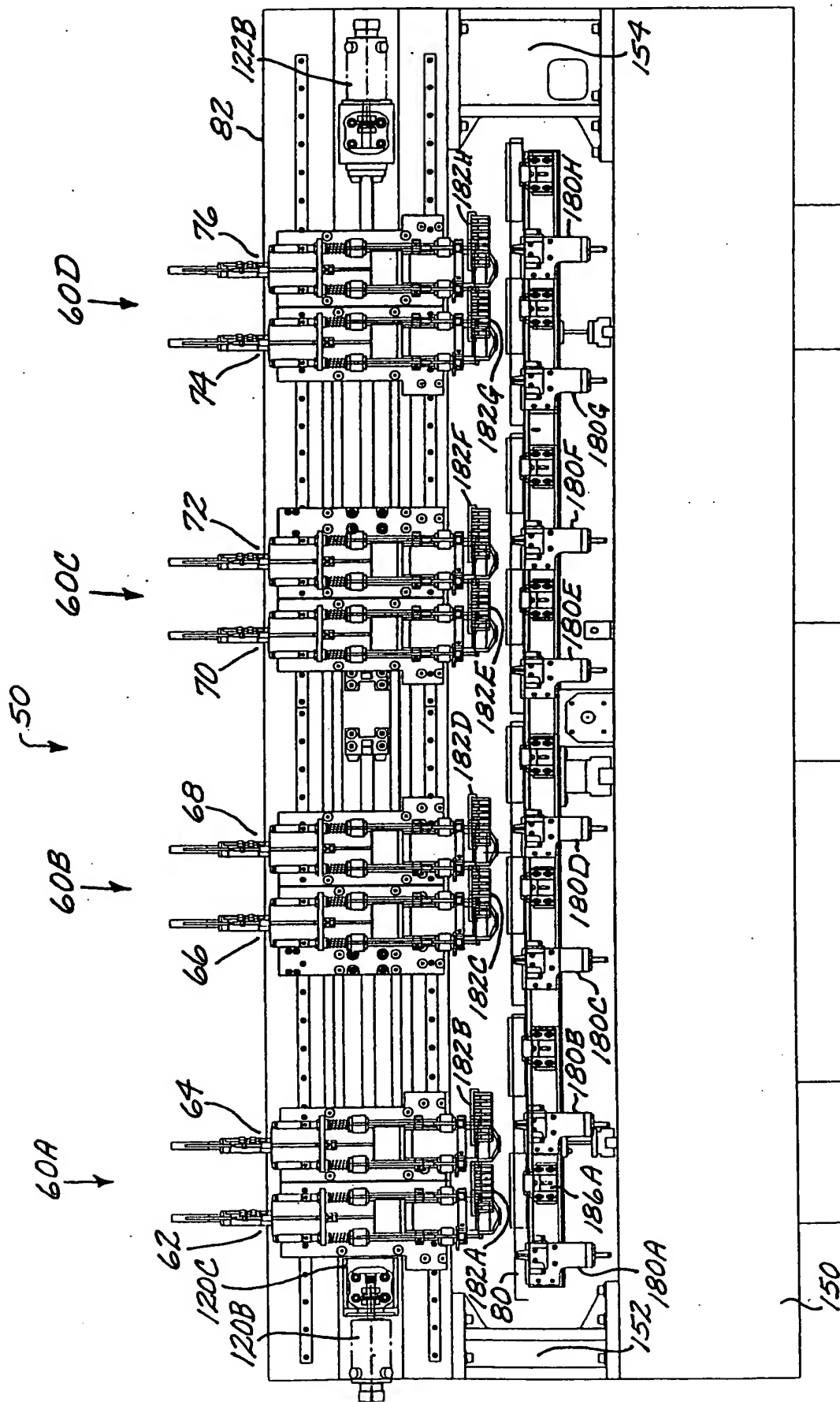
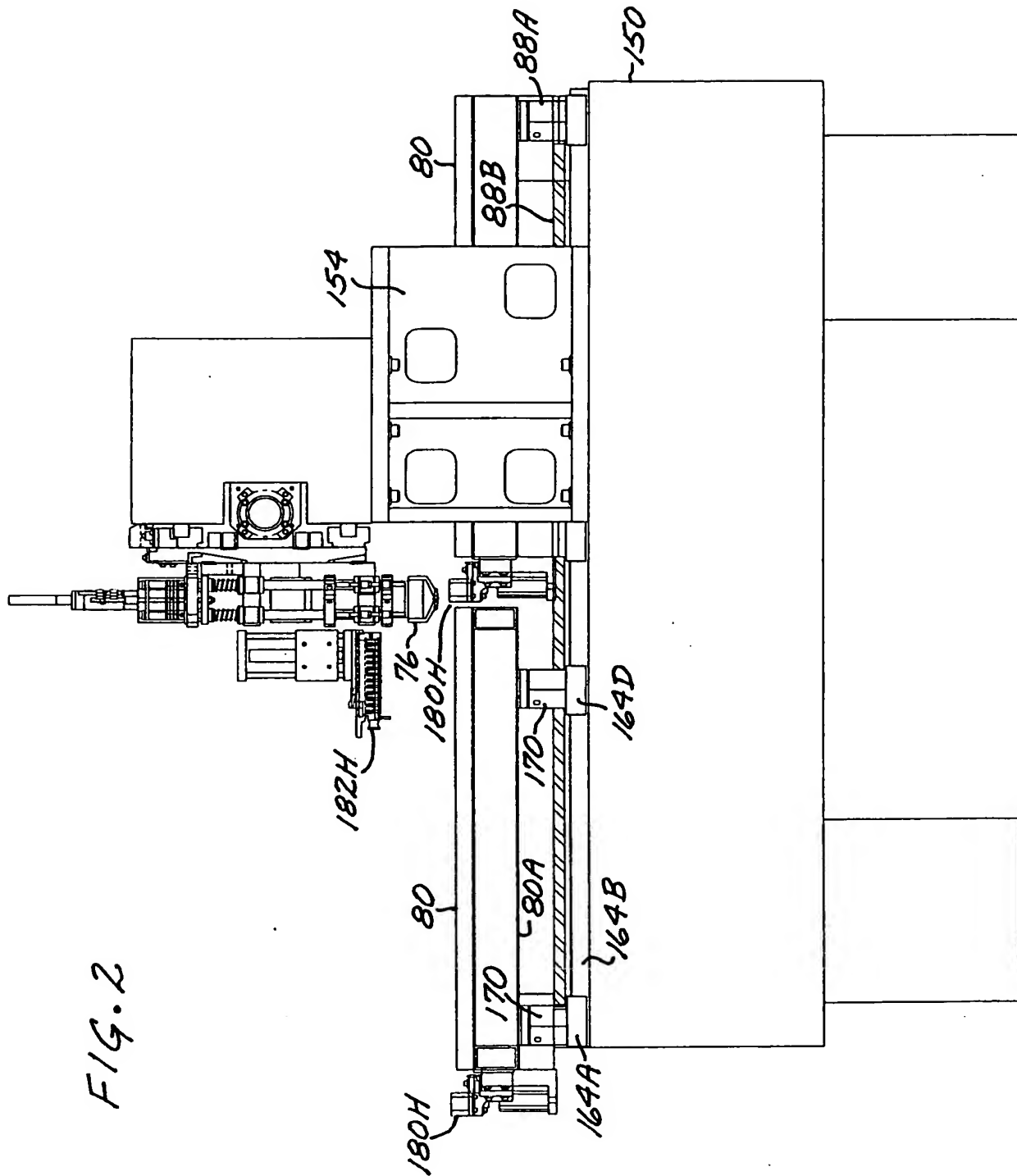


FIG. 2



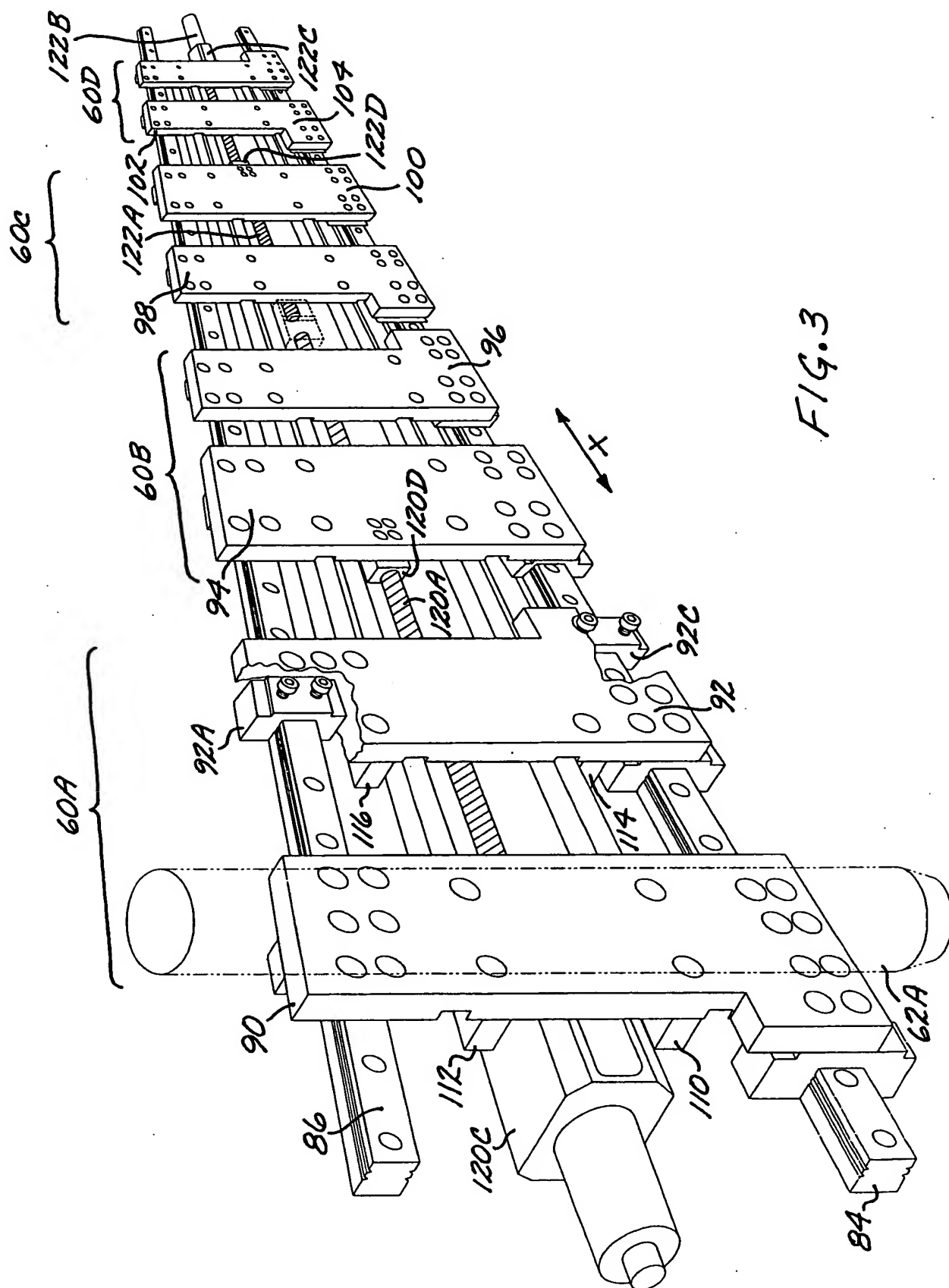
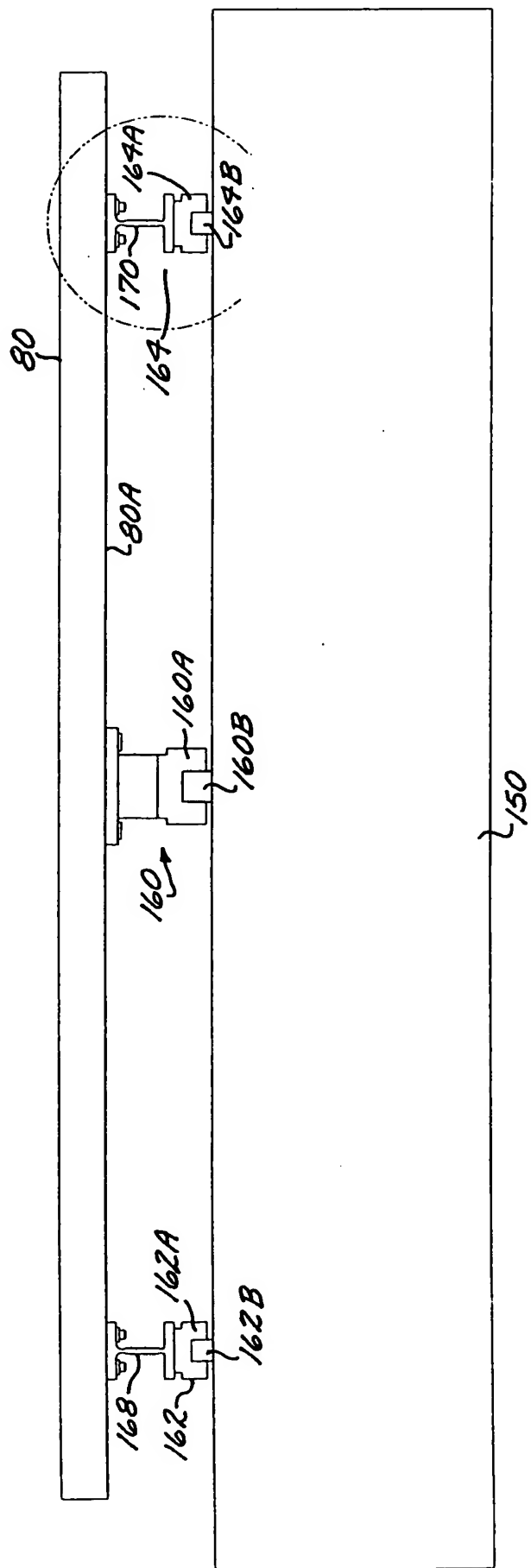


FIG. 3

FIG. 4



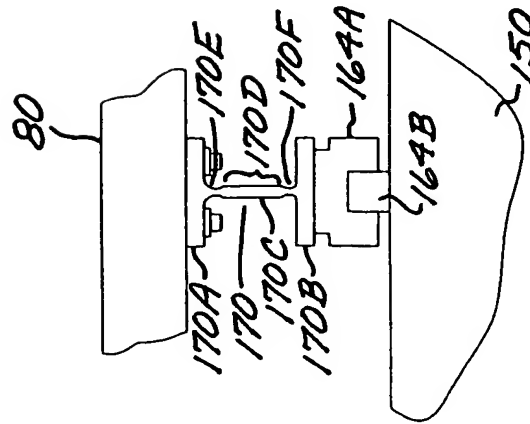


FIG. 5

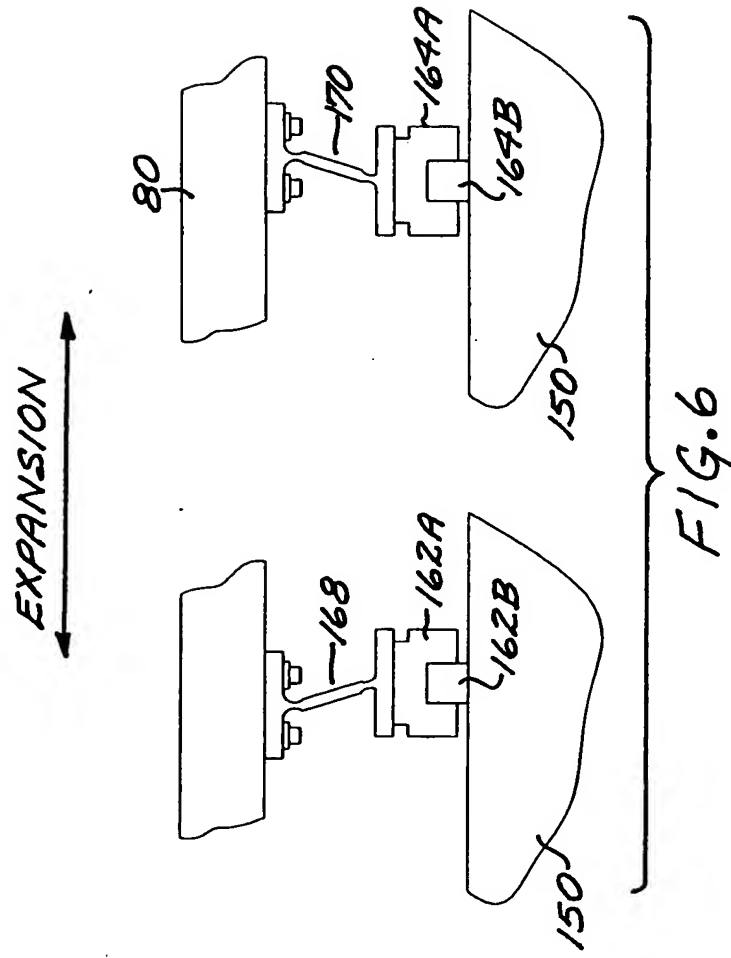
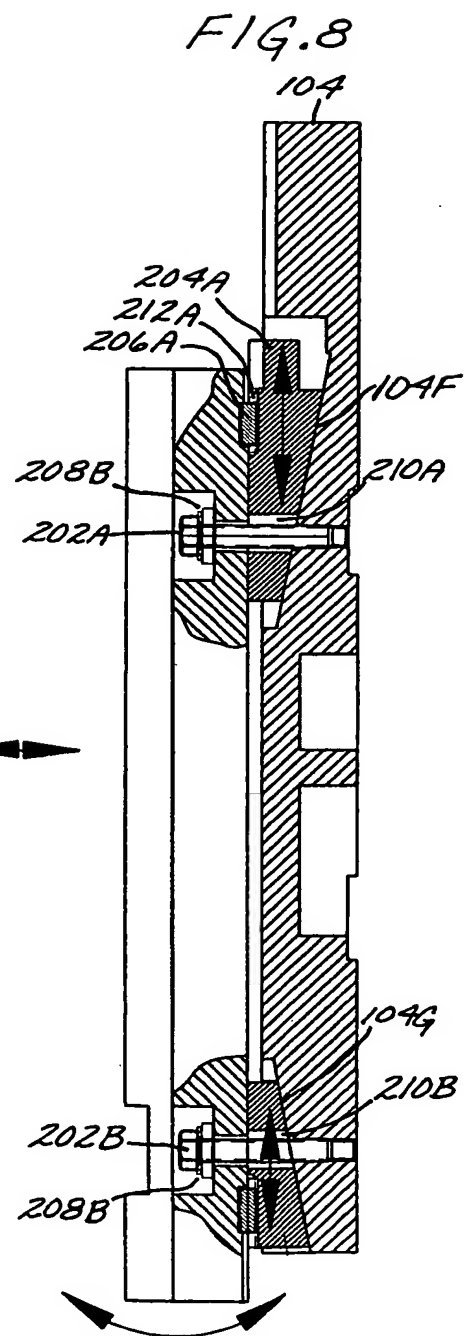
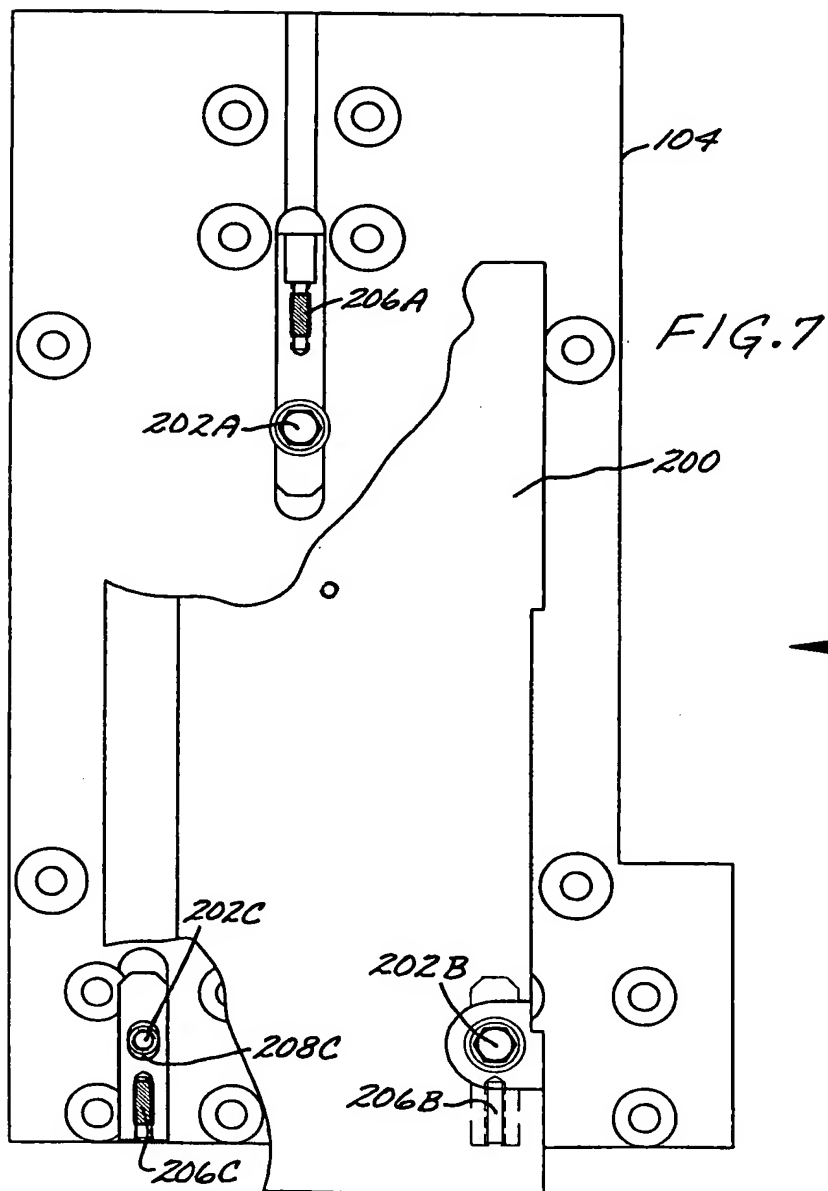
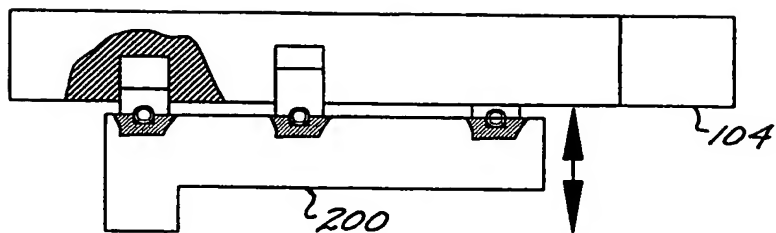


FIG. 6



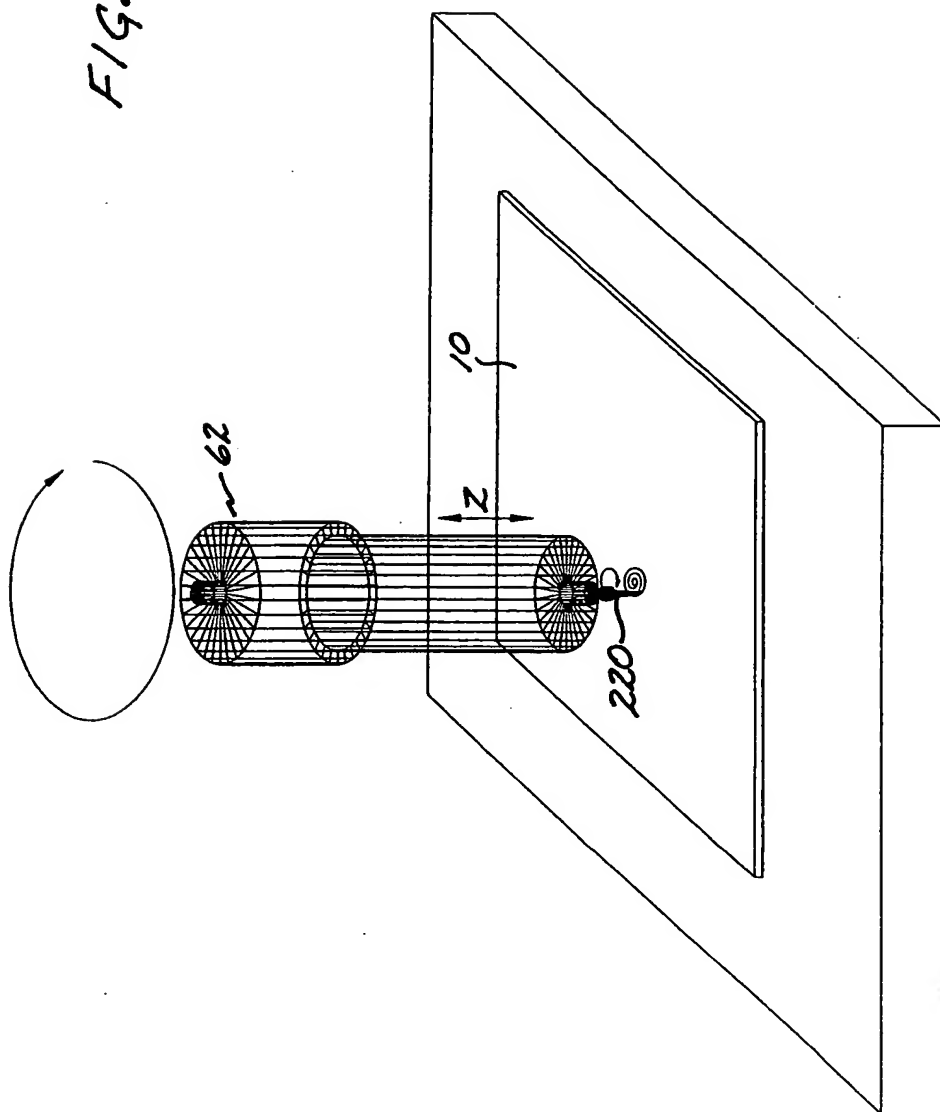
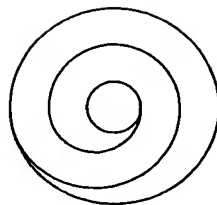


FIG. 10



ROUTER PATH THROUGH THE
MATERIAL TO EVACUATE HOLE
VOLUME IN CHIP FORM.

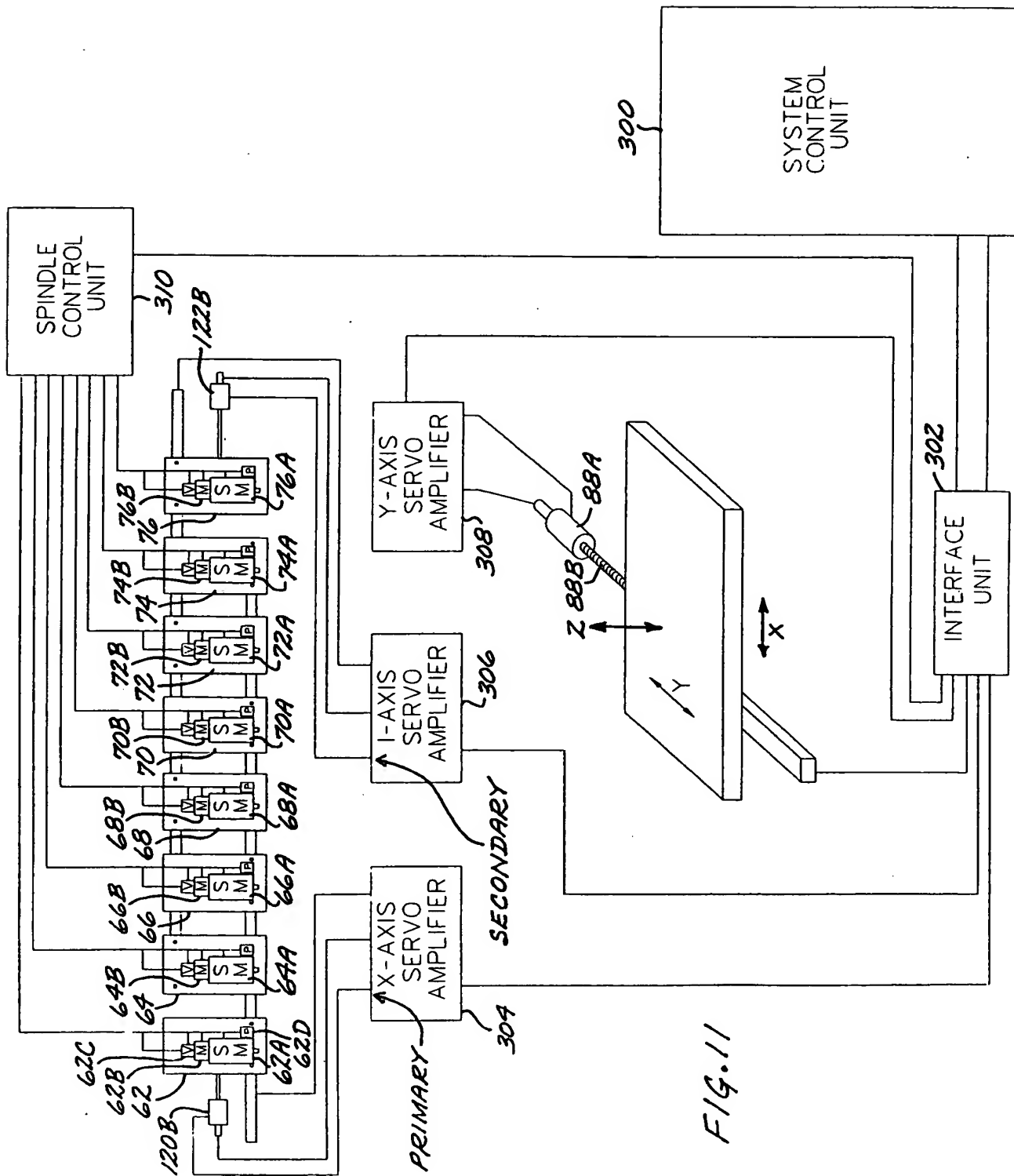


FIG. 12

60A

SP.2

62

SP.1

64

12

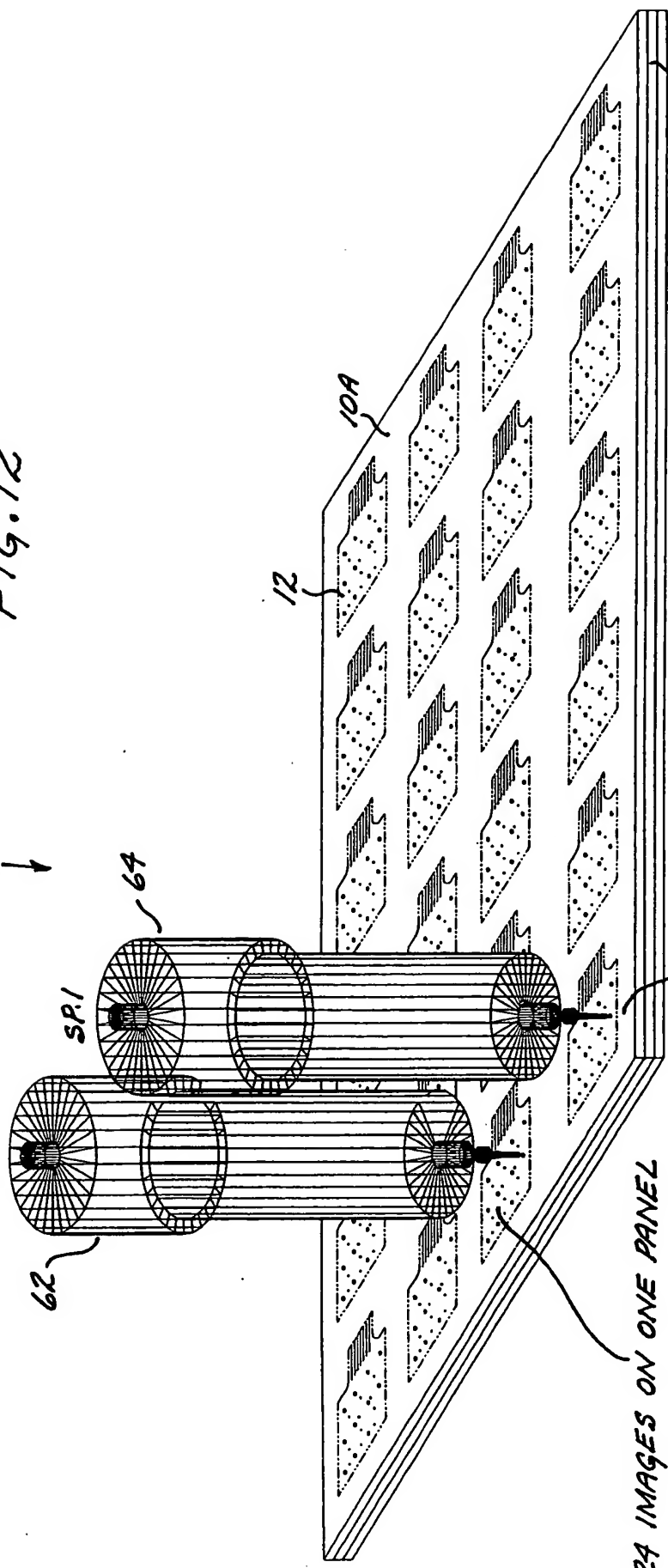
10A

10C

10B

24 IMAGES ON ONE PANEL

DRILLED PANEL, PROCESSED WITH
24 STEP AND REPEAT IMAGES



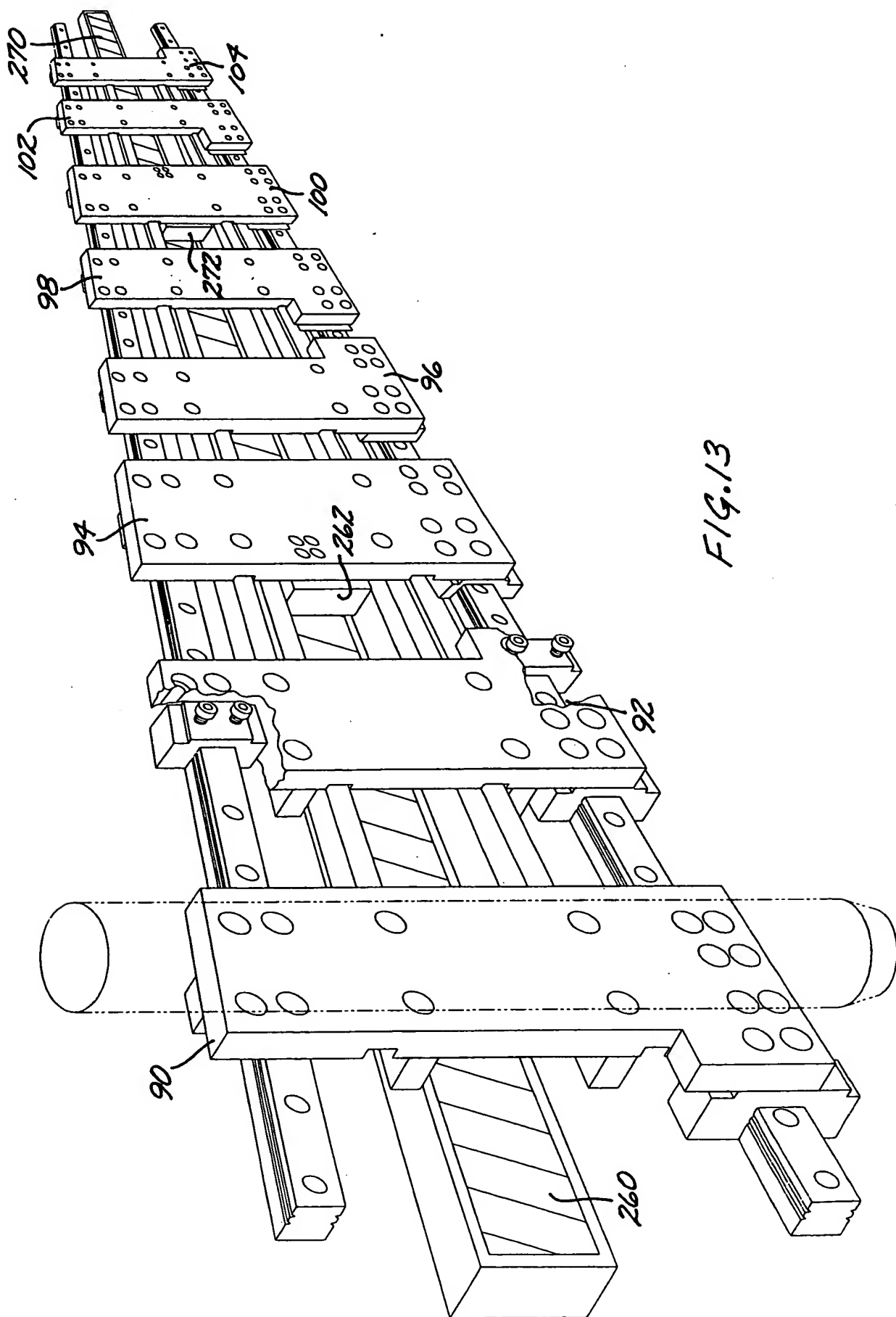


FIG. 13